

"Method And System For Implementing A  
Reduced Complexity Dual Rate Echo Canceller"  
Applicants: Bijit Hadler, et al.  
Application No. 10/020,134  
Attorney Docket No. 56162.000358

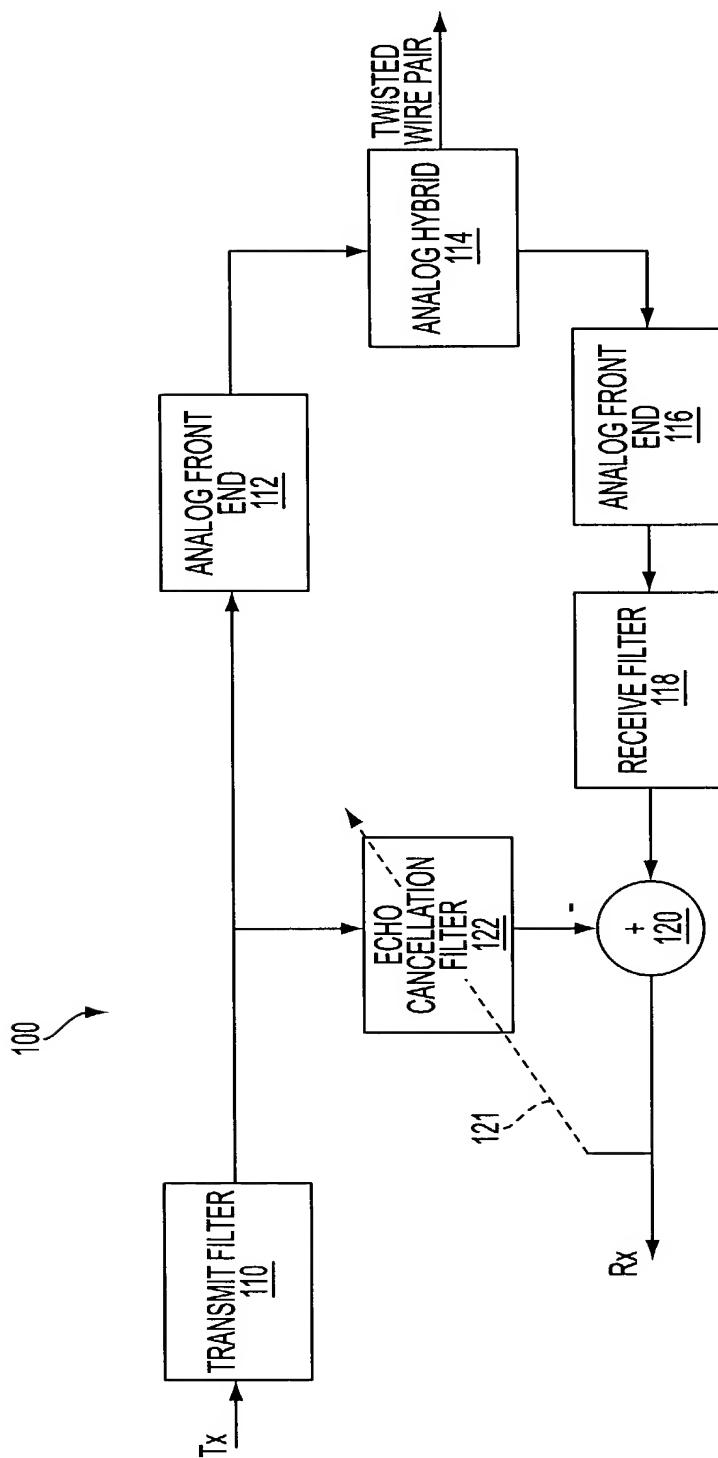


FIG. 1A

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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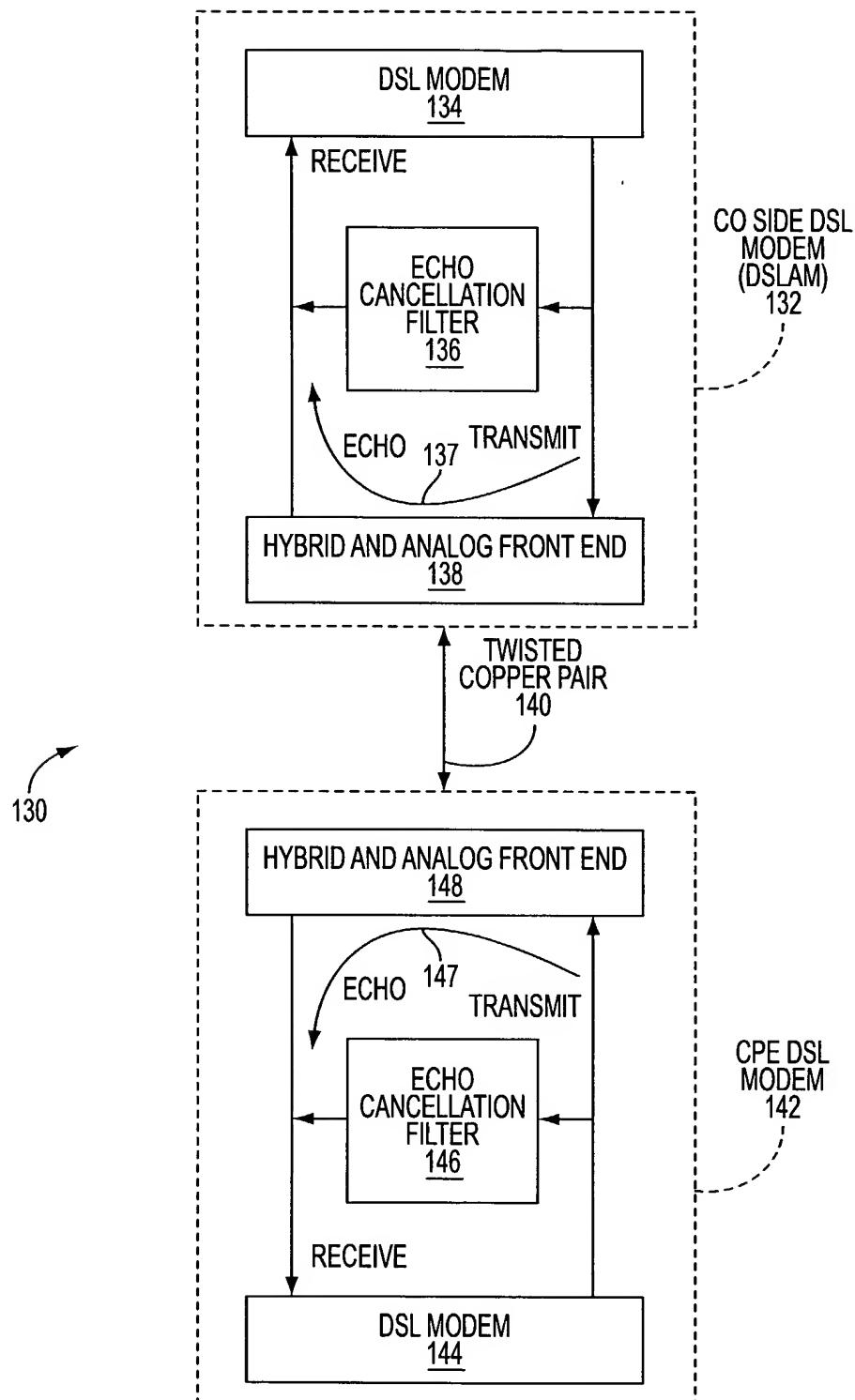


FIG. 1B

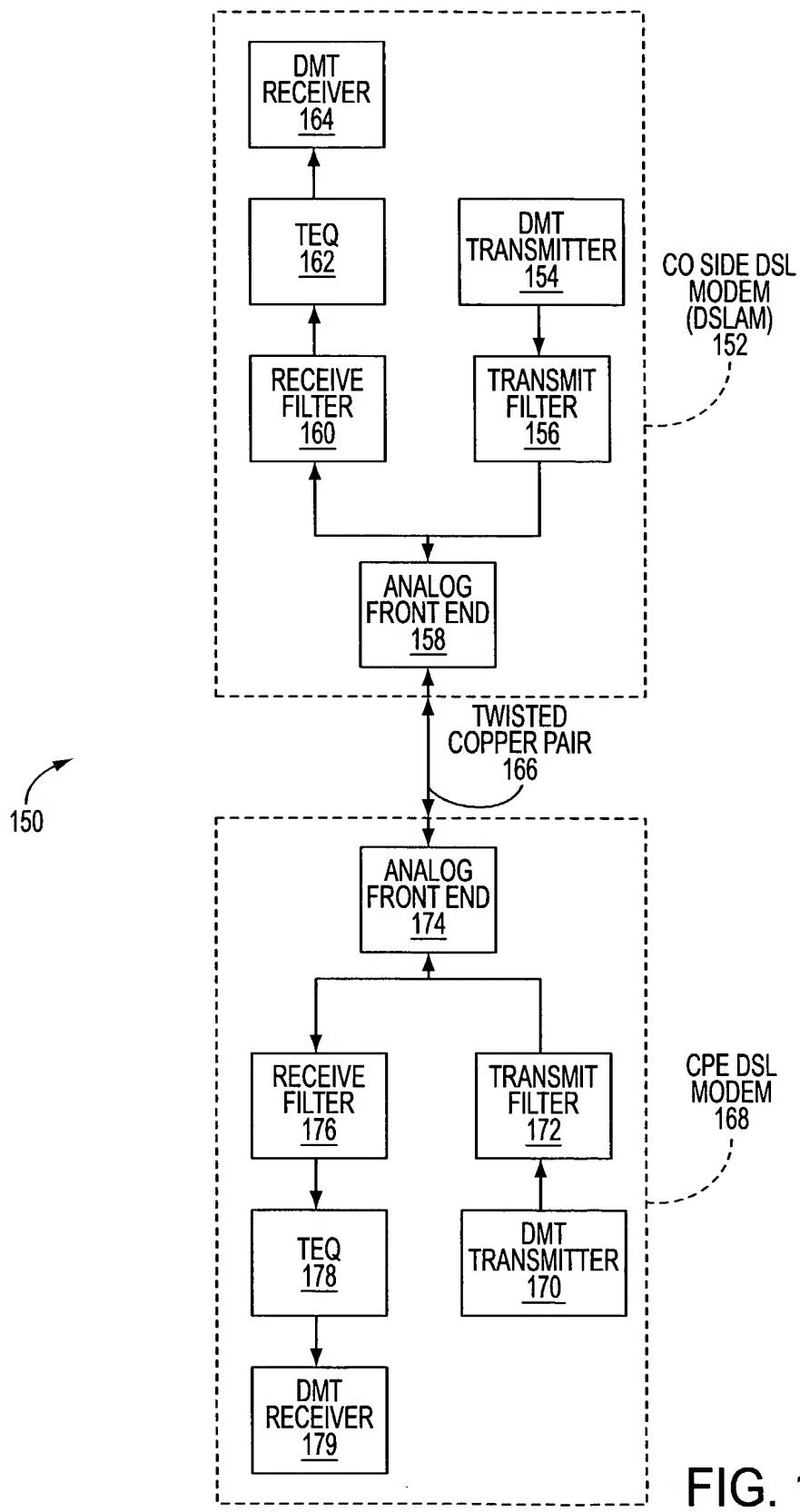


FIG. 1C

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"  
Applicants: Bijit Hadler, et al.  
Application No. 10/020,134  
Attorney Docket No. 56162.000358

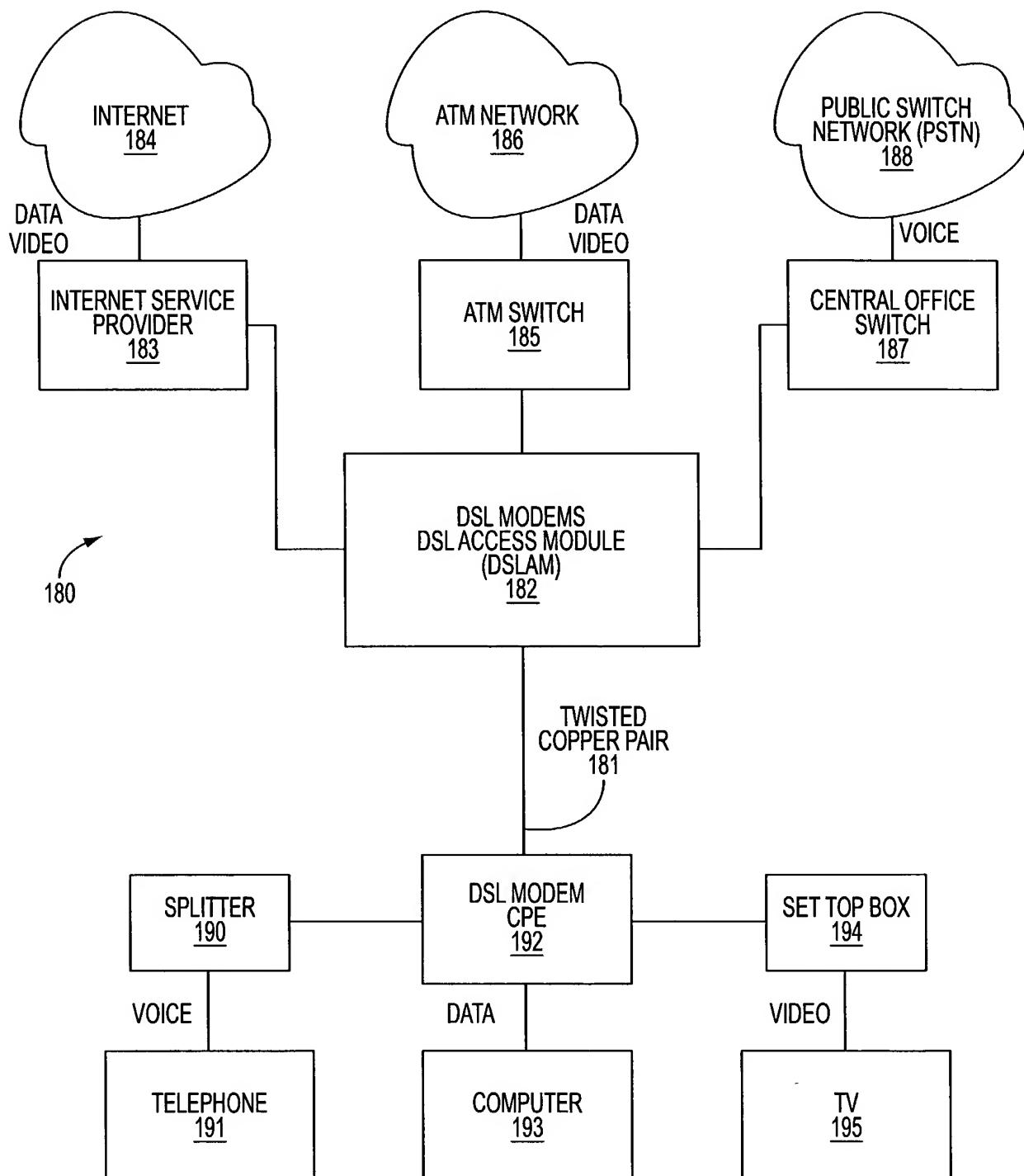


FIG. 1D

“Method And System For Implementing A  
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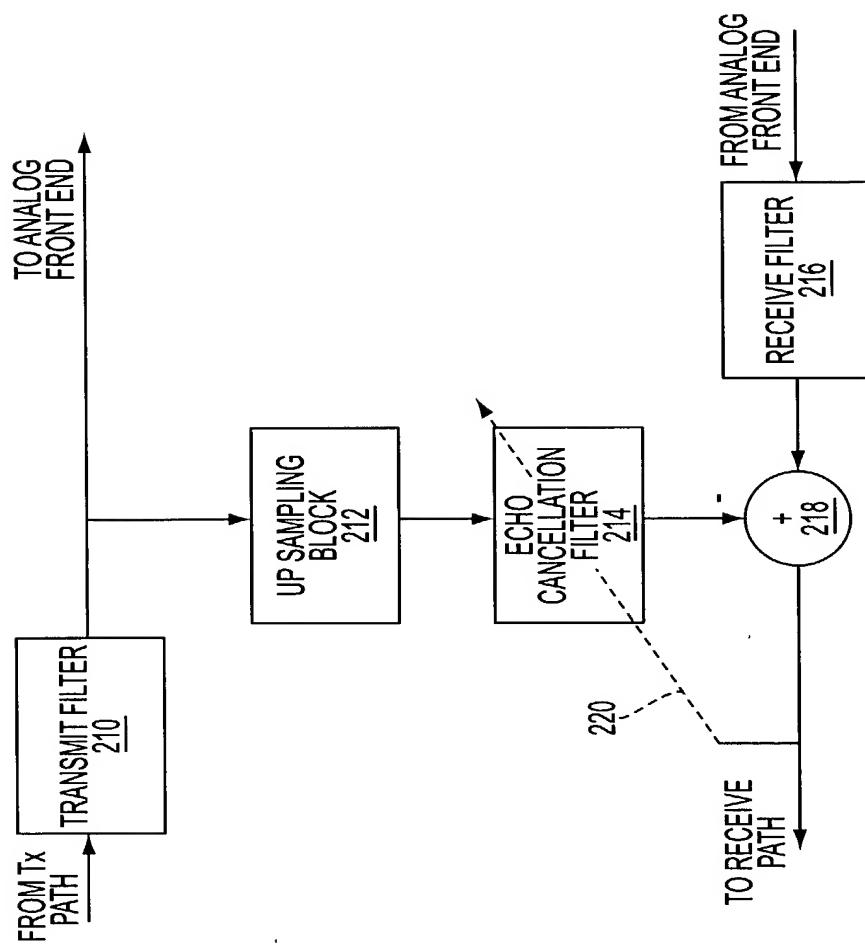


FIG. 2

# “Method And System For Implementing A Reduced Complexity Dual Rate Echo

**Canceller"**

Applicants: Bijit Hadler, et al.

Application No. 10/020,134  
Attorney Docket No. 56162-000258

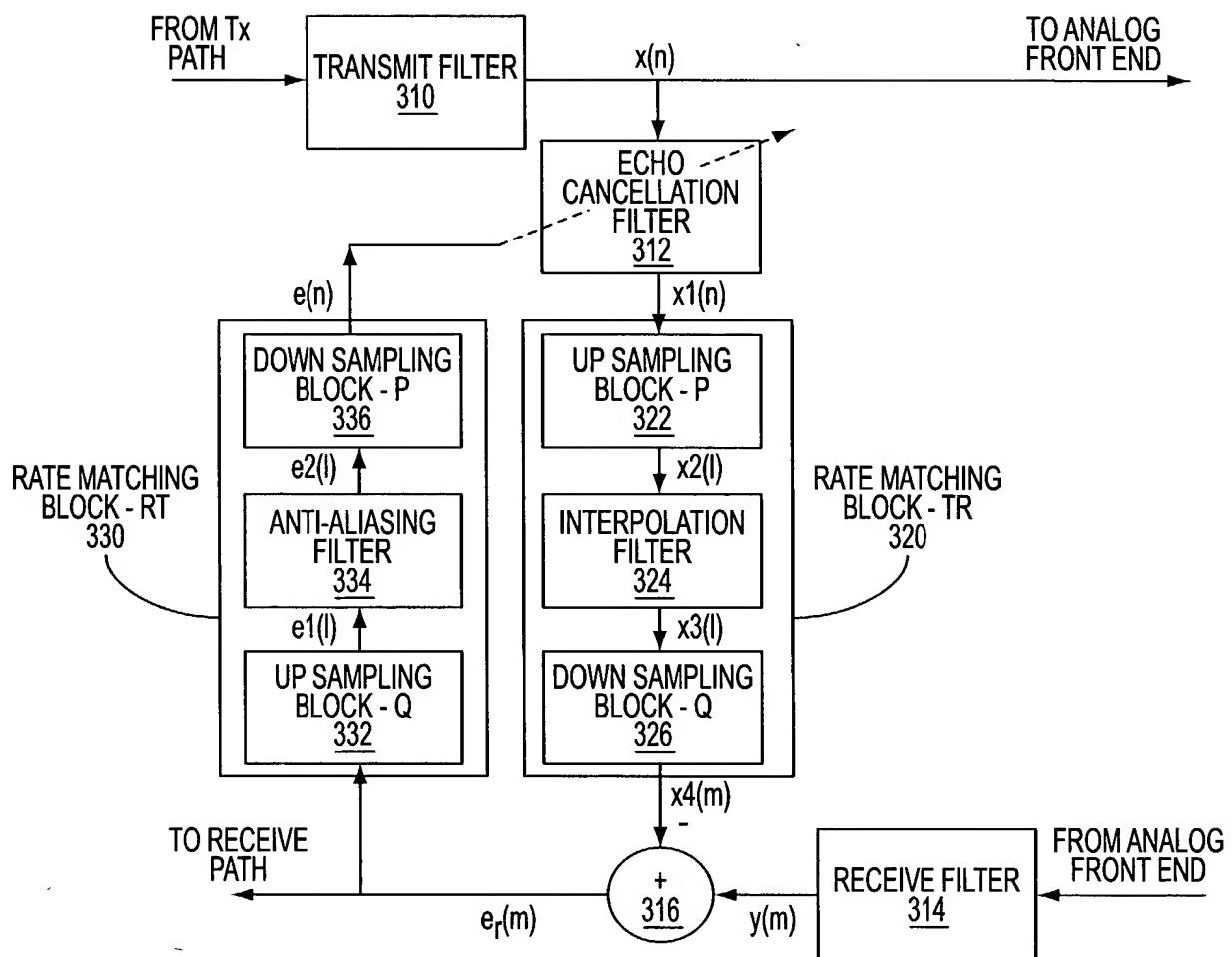


FIG. 3

"Method And System For Implementing A  
 Reduced Complexity Dual Rate Echo Cancellation Filter, in a Digital Baseband Processor"  
 Applicants: Bijit Hadler, et al.  
 Application No. 10/020,134  
 Attorney Docket No. 56162.000358

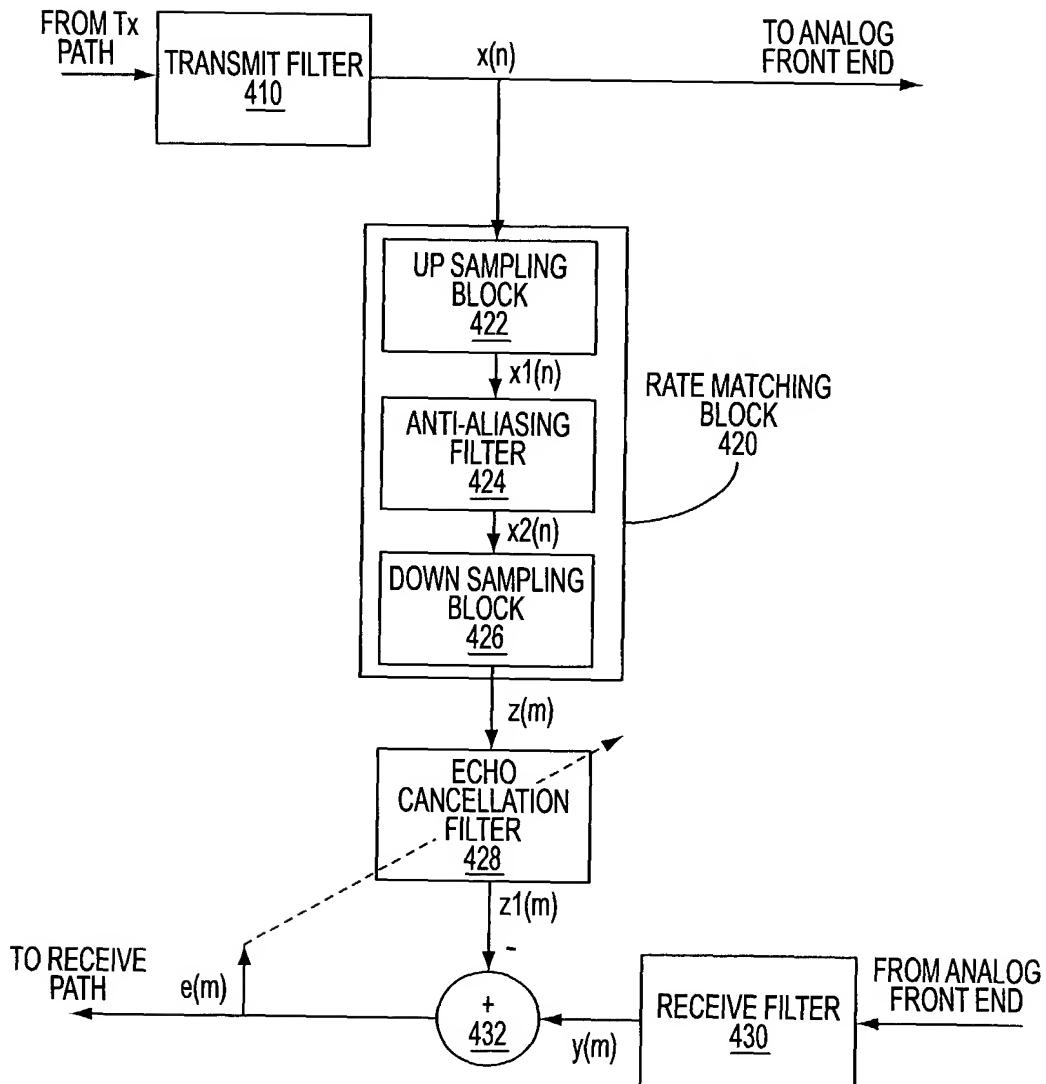
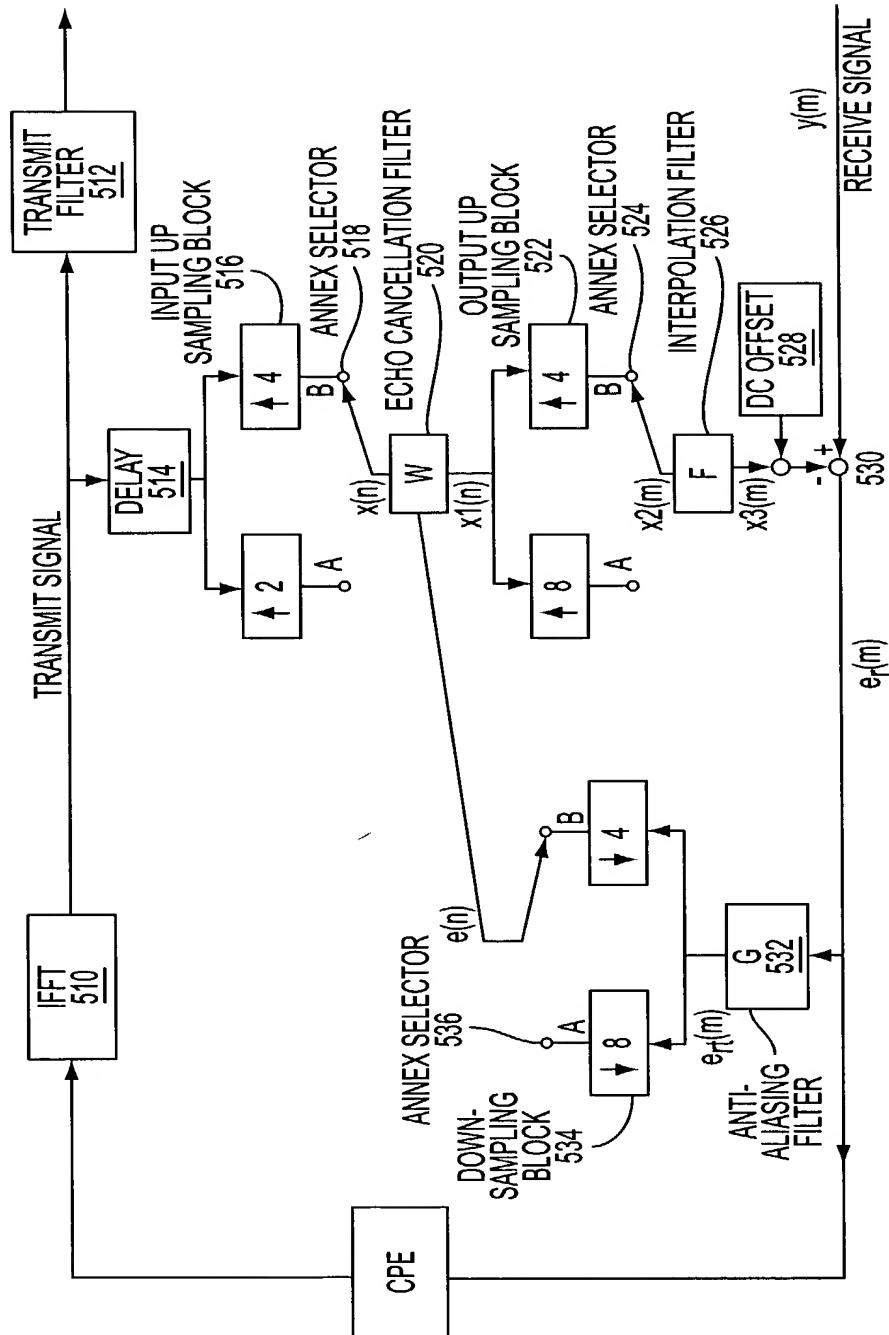


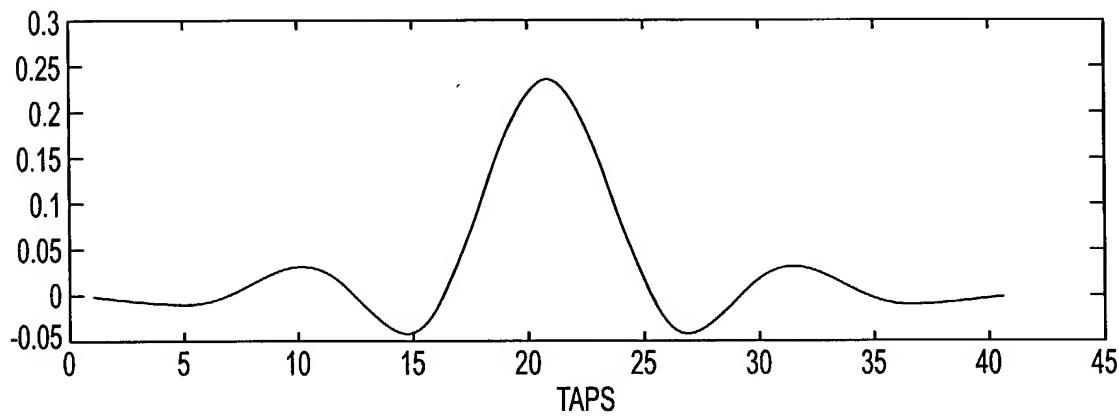
FIG. 4

**"Method And System For Implementing A  
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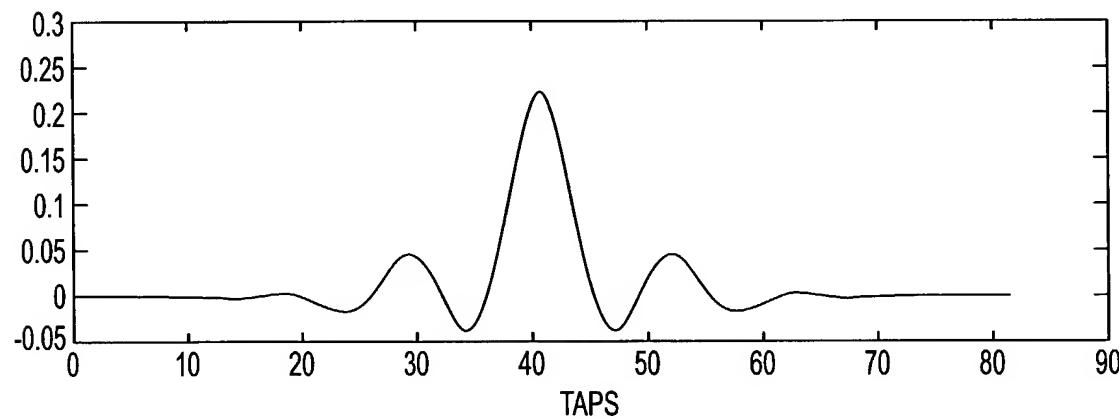


**FIG. 5**

**"Method And System For Implementing A  
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**FIG. 6**



**FIG. 7**

"Method And System For Implementing A  
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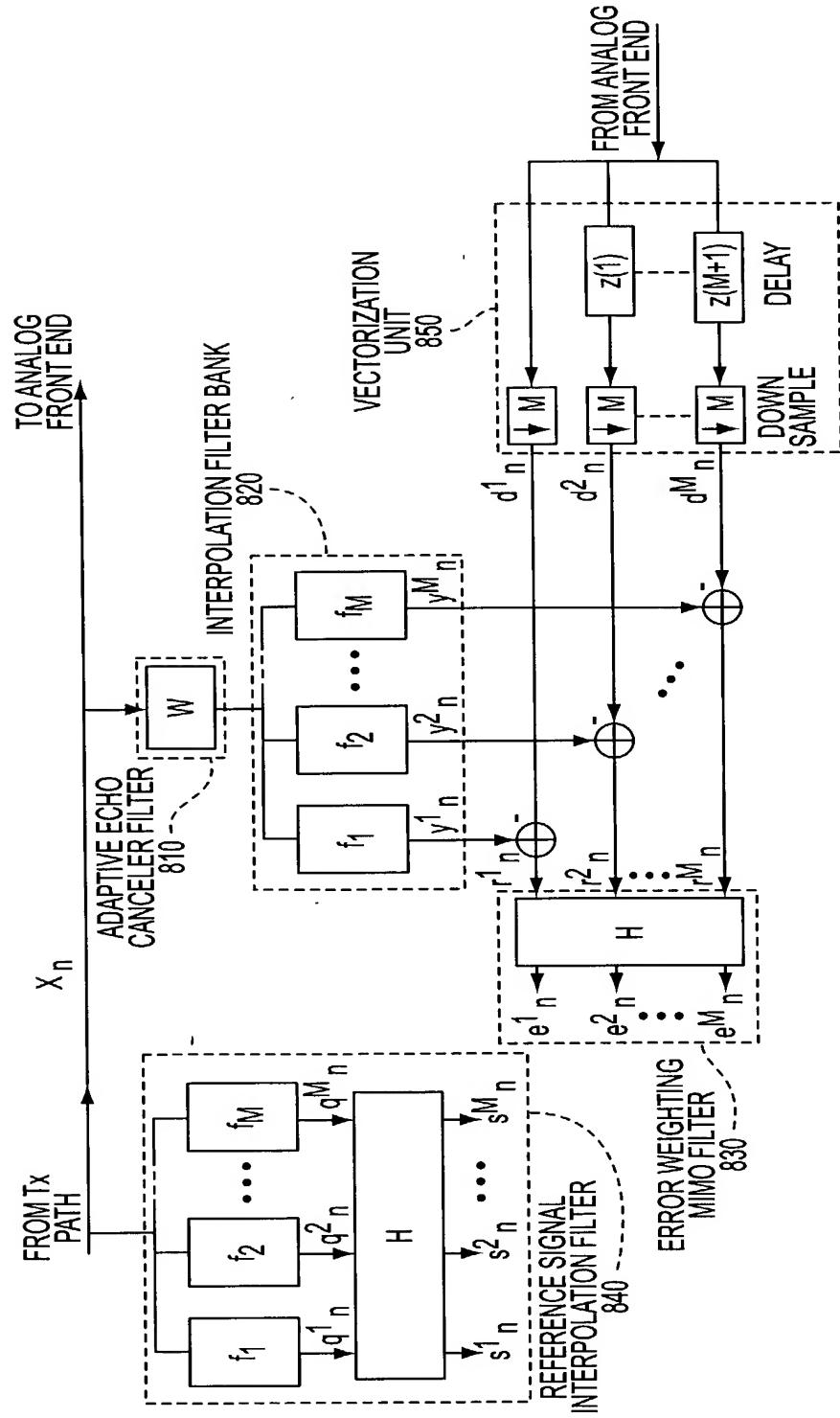


FIG. 8

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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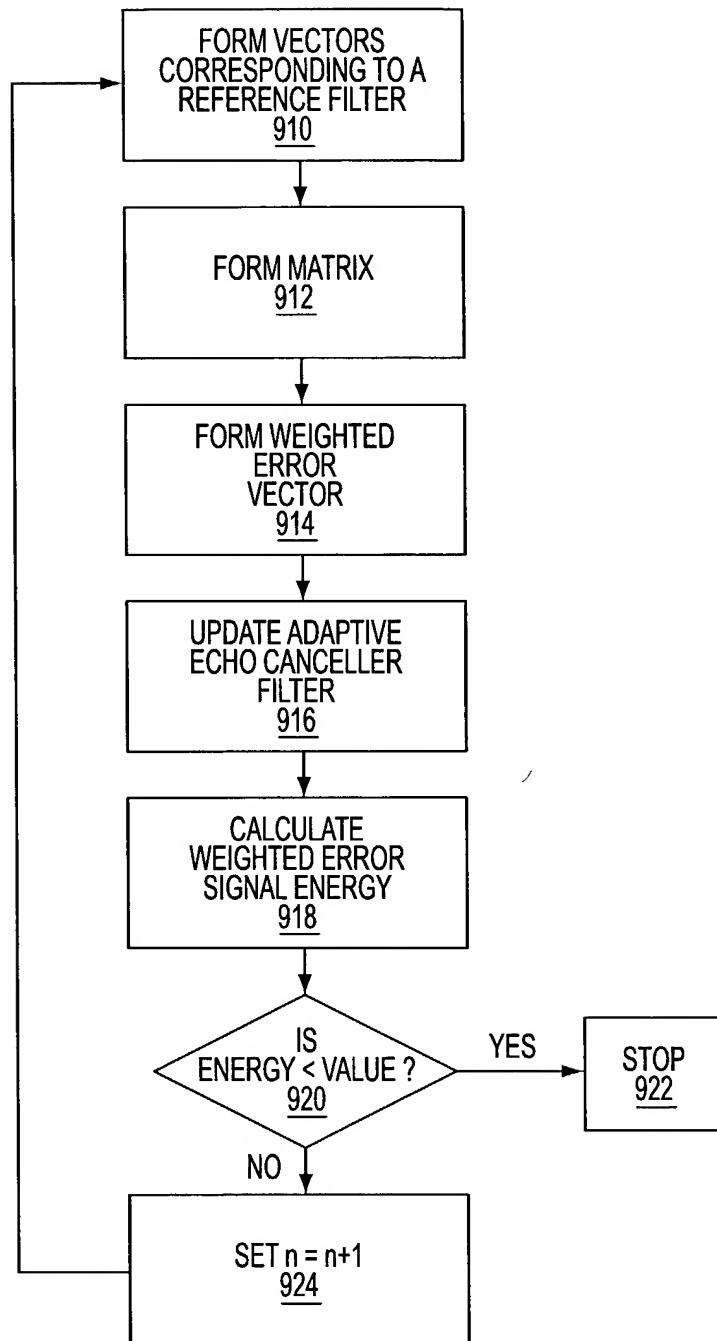


FIG. 9

"Method And System For Implementing A  
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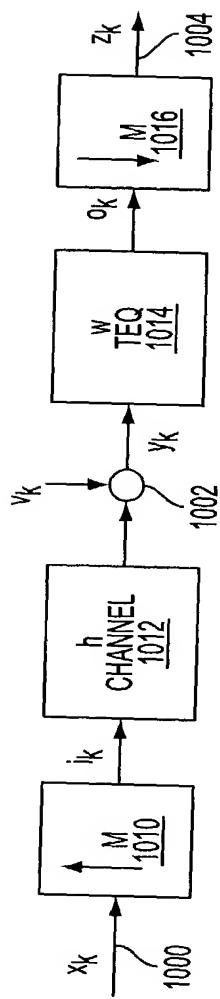


FIG. 10A

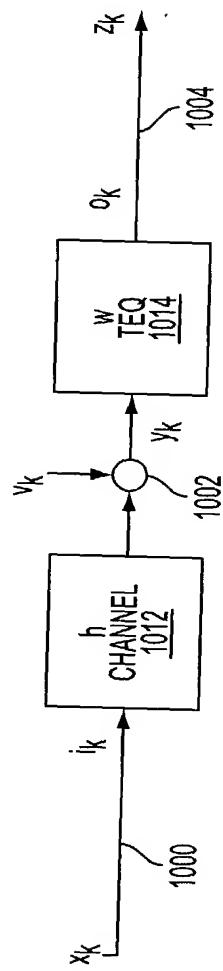


FIG. 10B

“Method And System For Implementing A  
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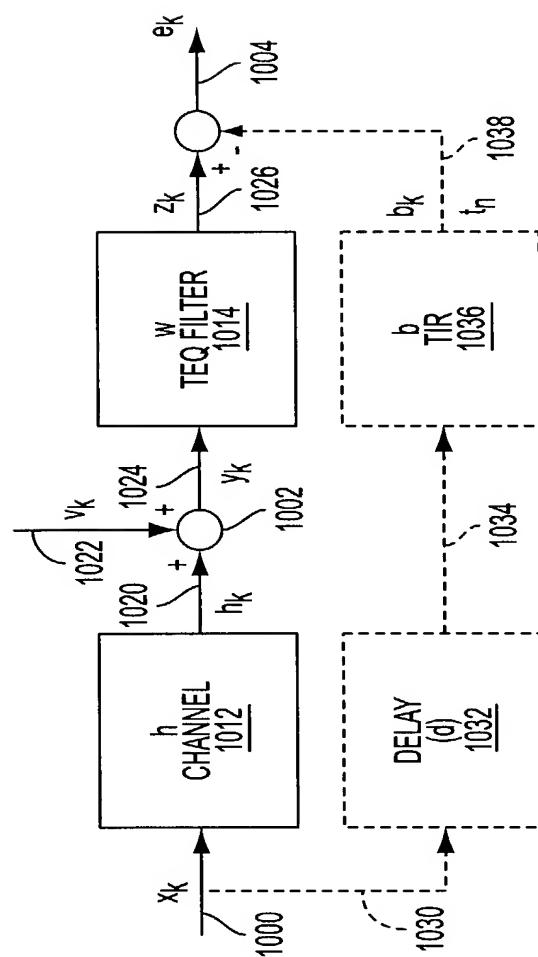


FIG. 10C

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"  
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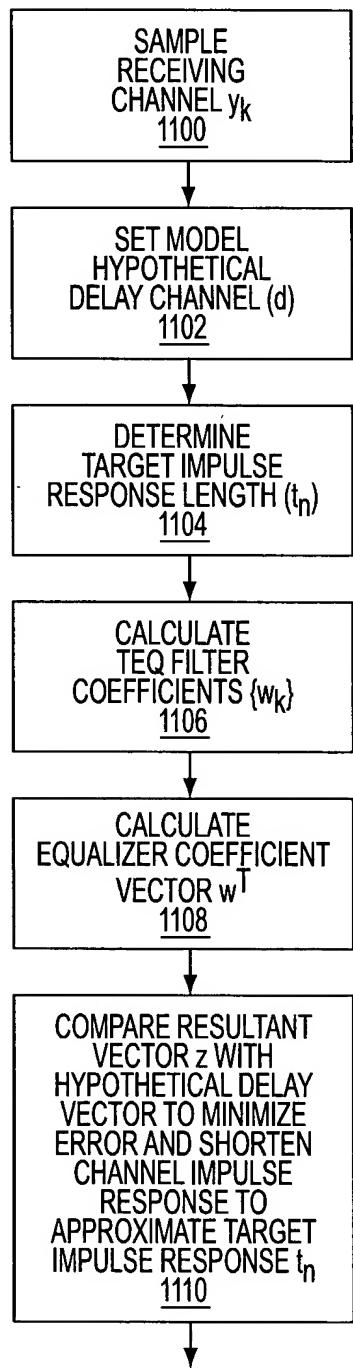


FIG. 11

"Method And System For Implementing A  
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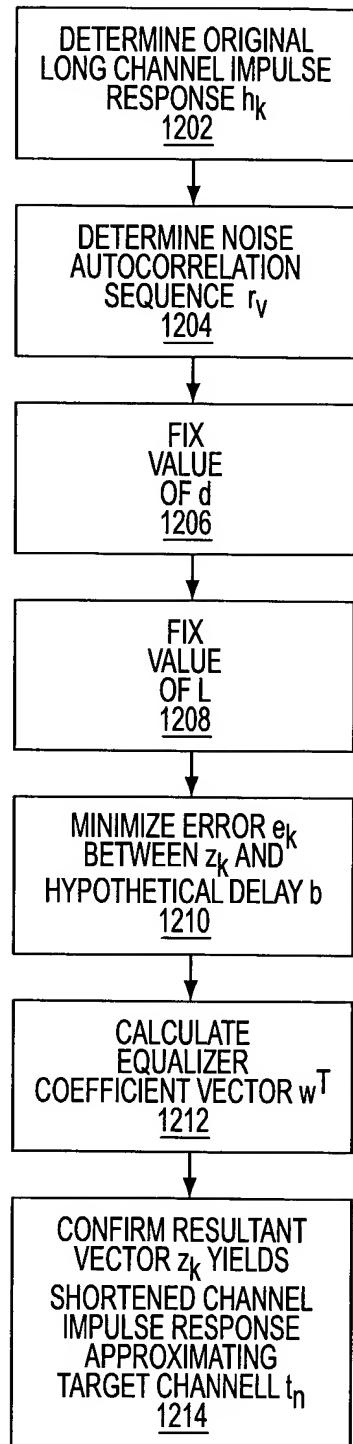


FIG. 12

"Method And System For Implementing A  
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 Canceller"  
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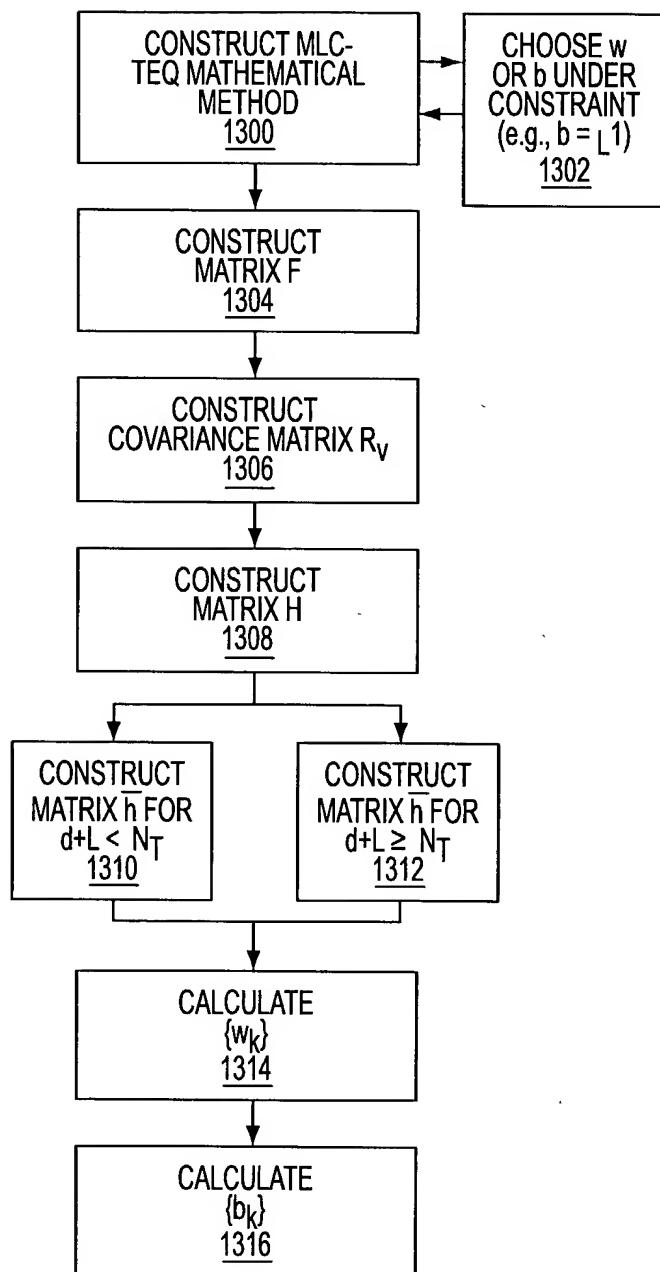


FIG. 13

"Method And System For Implementing A  
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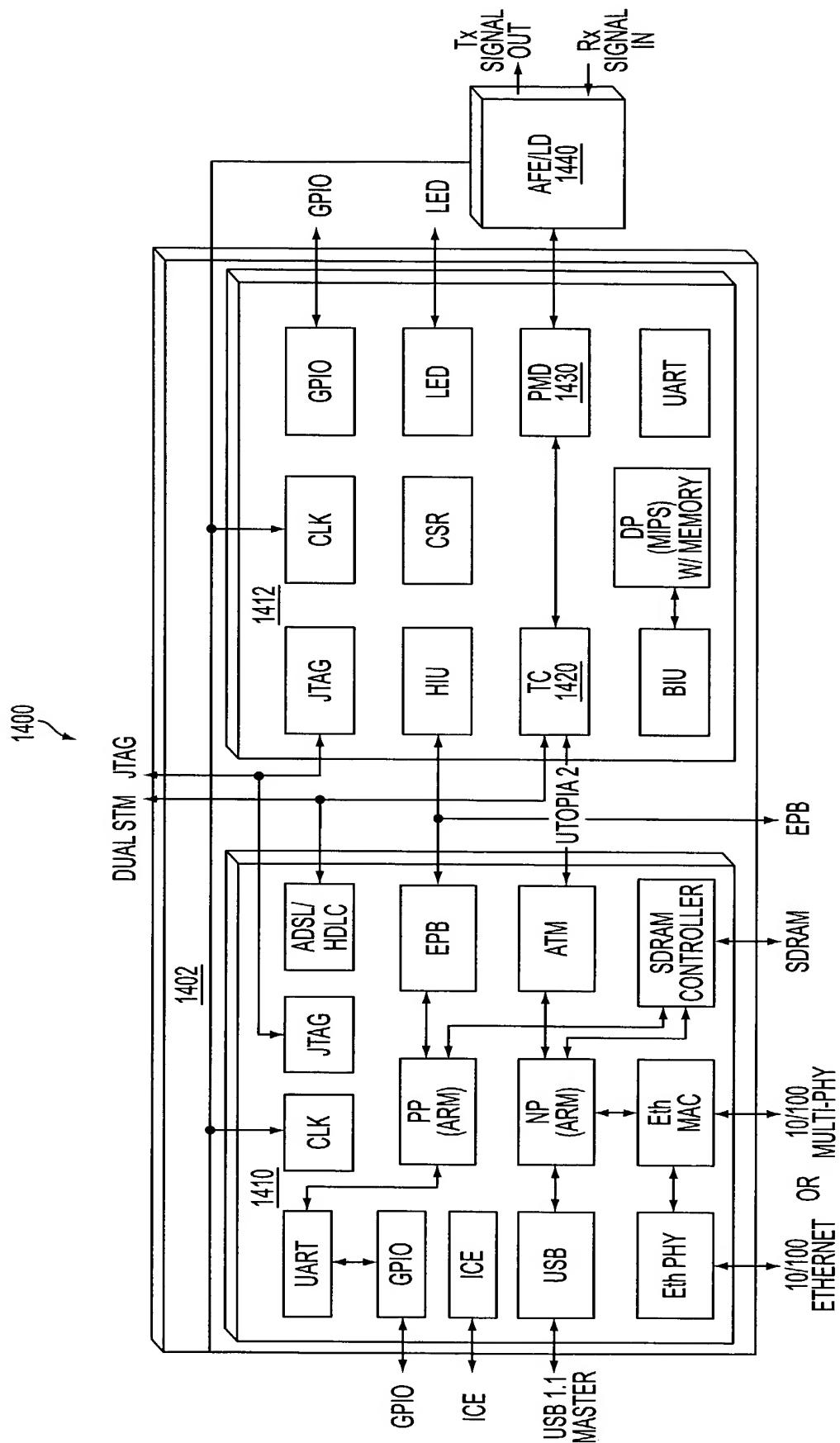


FIG. 14A

"Method And System For Implementing A  
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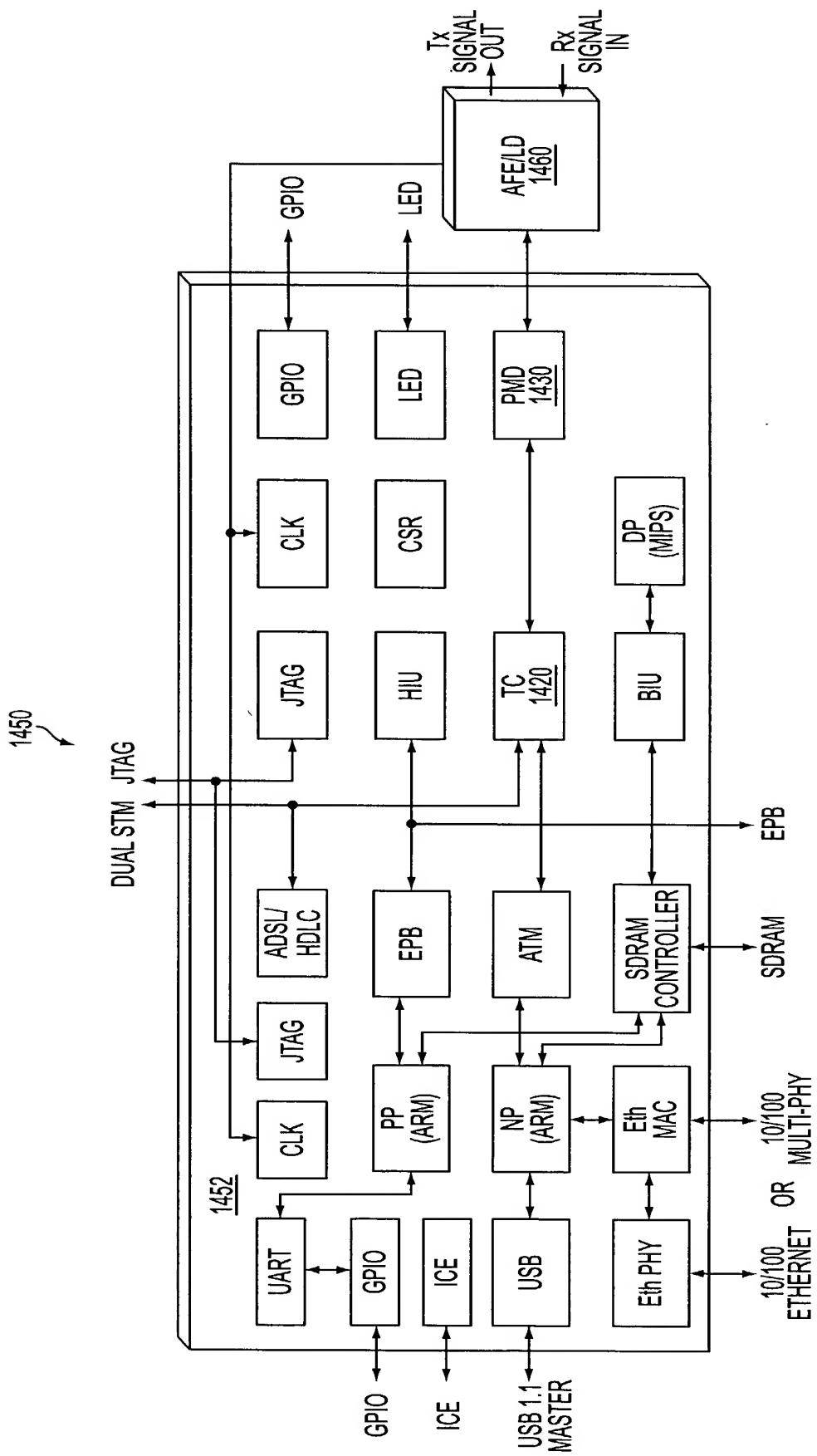


FIG. 14B

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"

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Attorney Docket No. 56162.000358

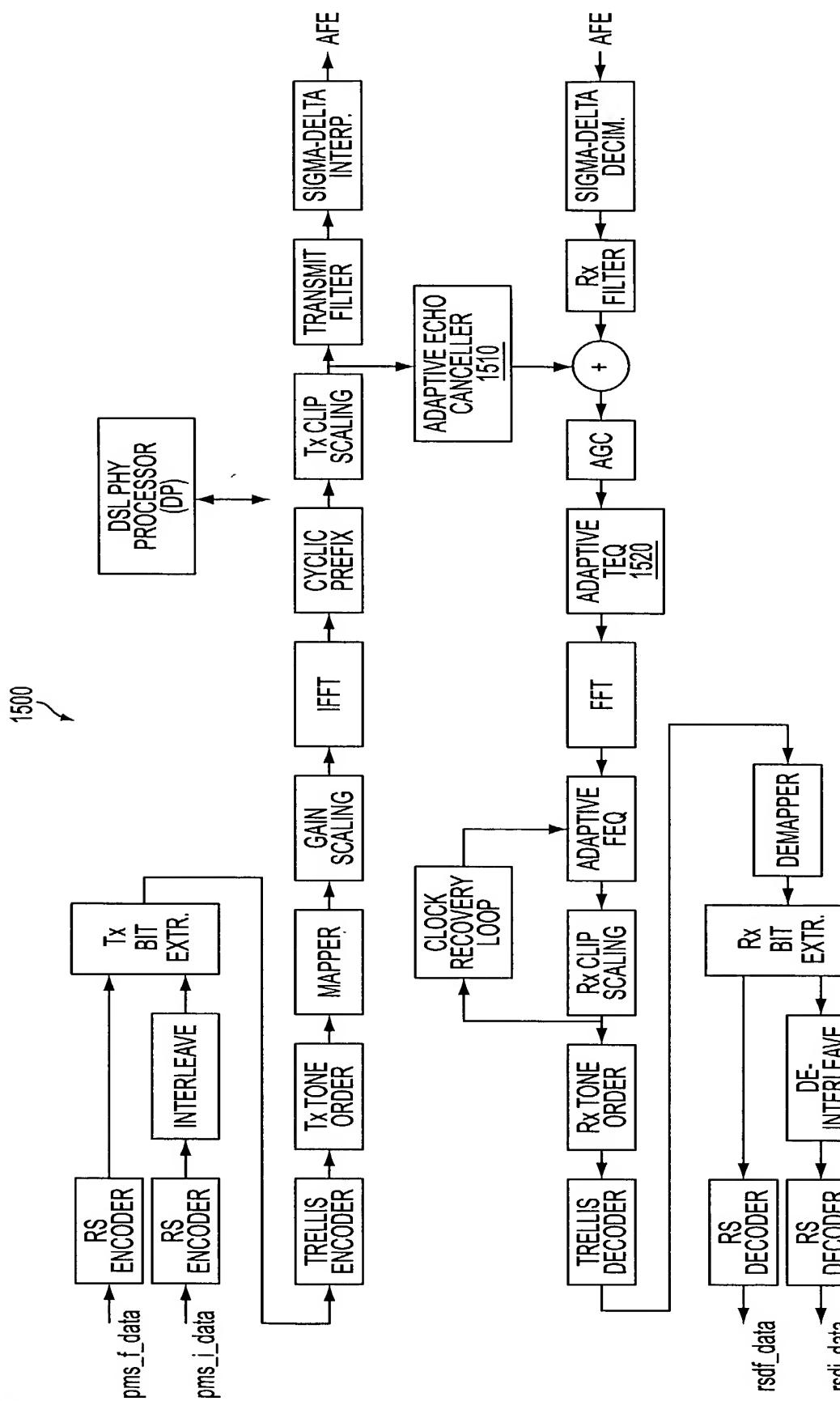


FIG. 15

"Method And System For Implementing A  
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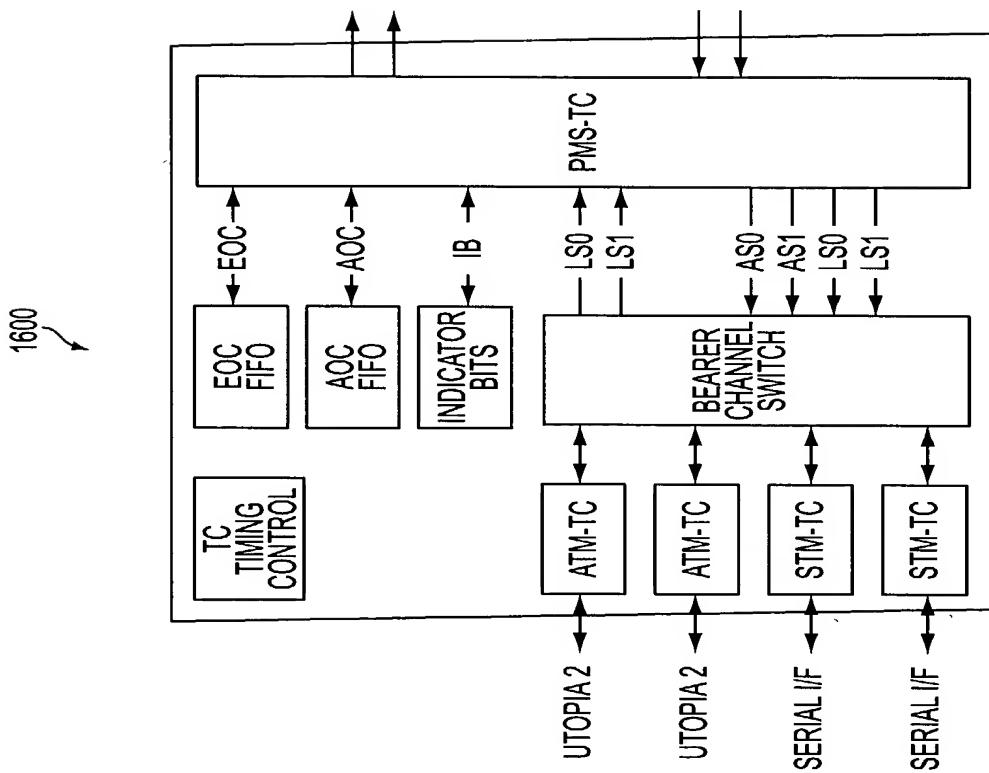


FIG. 16

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"  
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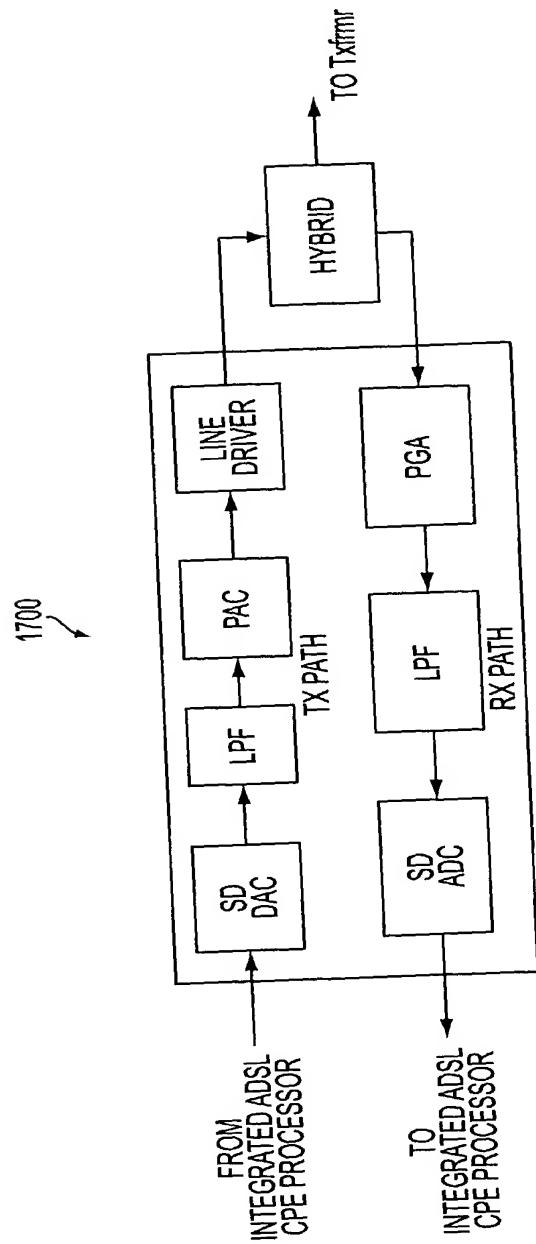
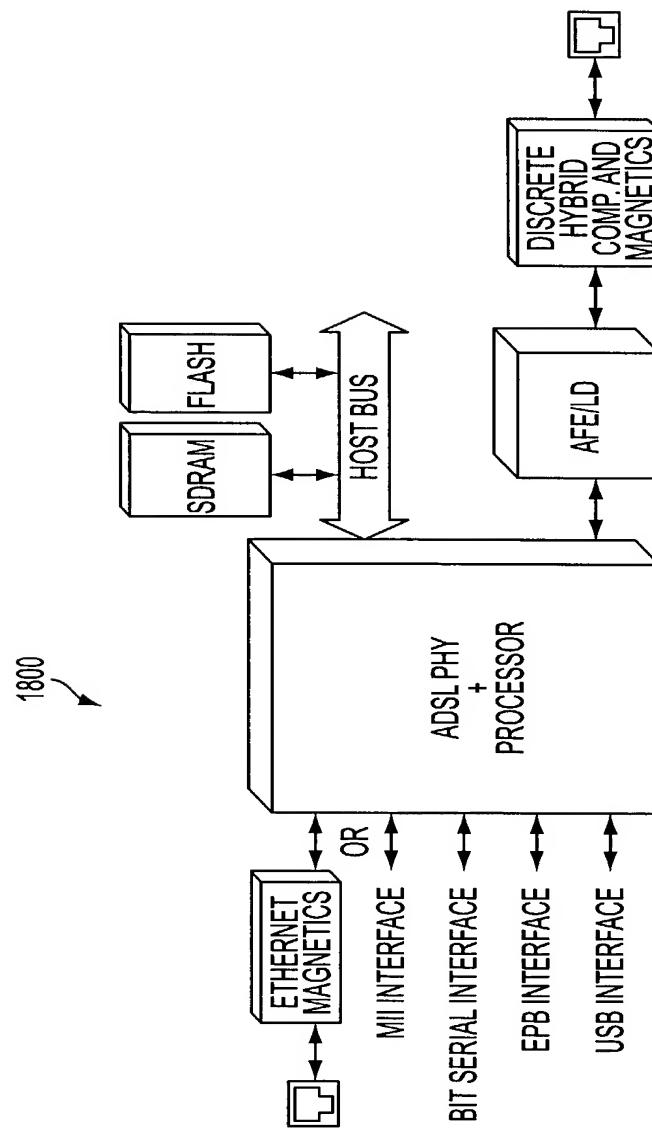


FIG. 17

**"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"**  
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**FIG. 18A**

"Method And System For Implementing A  
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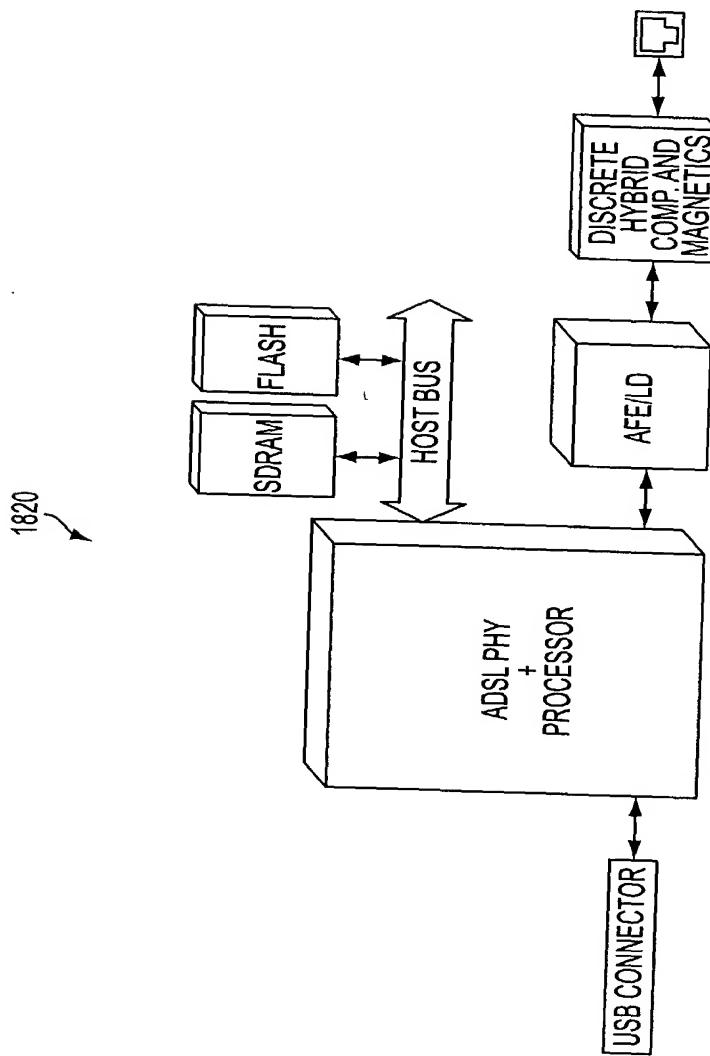


FIG. 18B

"Method And System For Implementing A Reduced Complexity Dual Rate Echo Canceller"  
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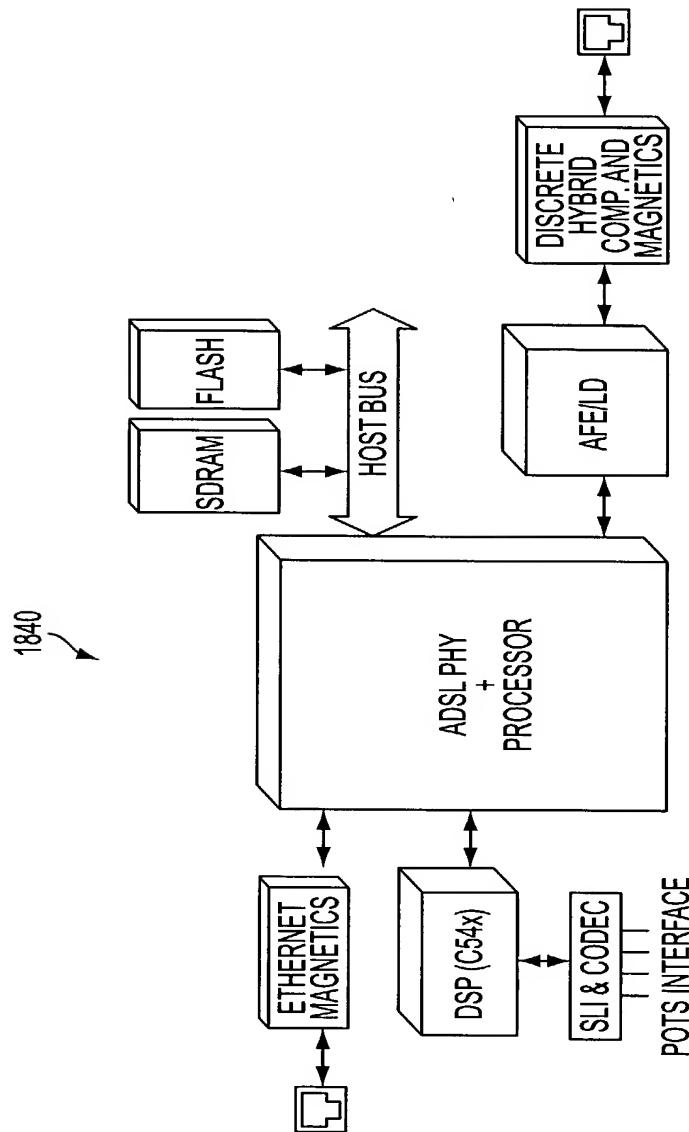


FIG. 18C